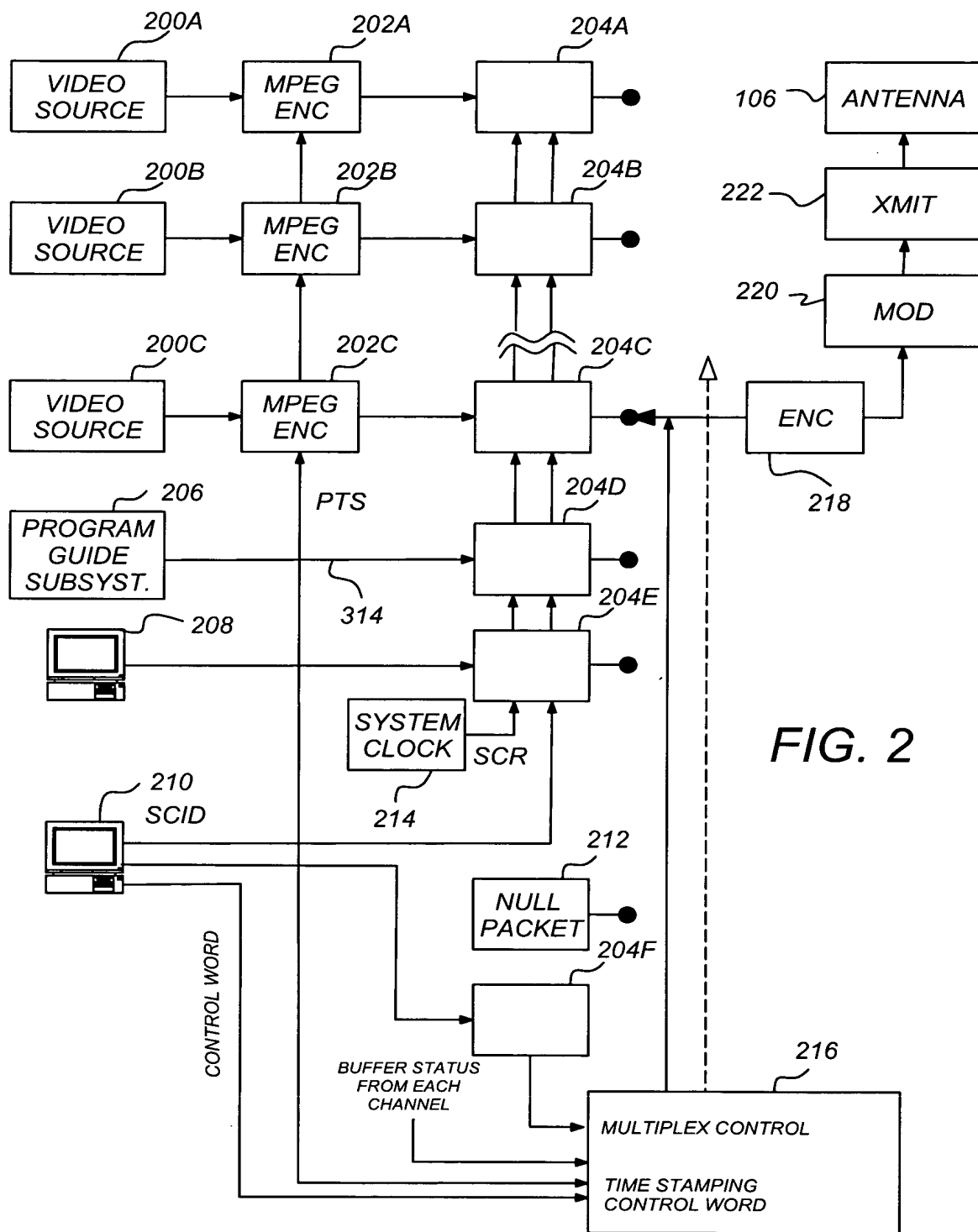


FIG. 1



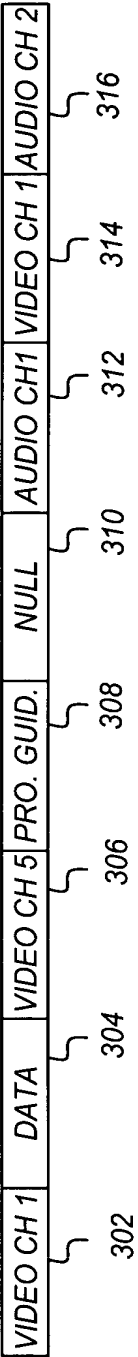


FIG. 3A

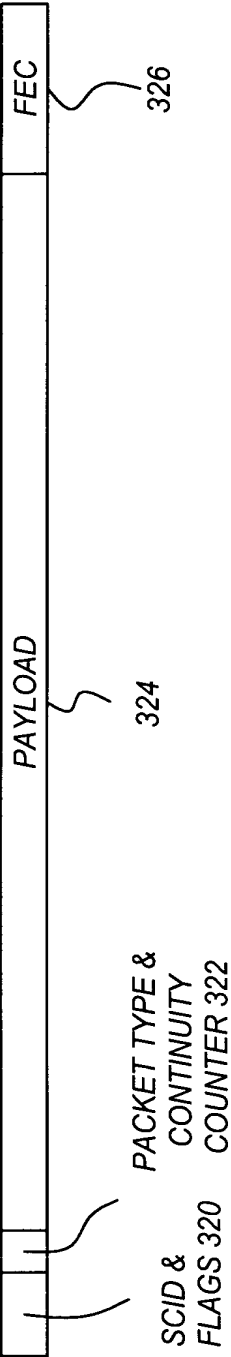


FIG. 3B

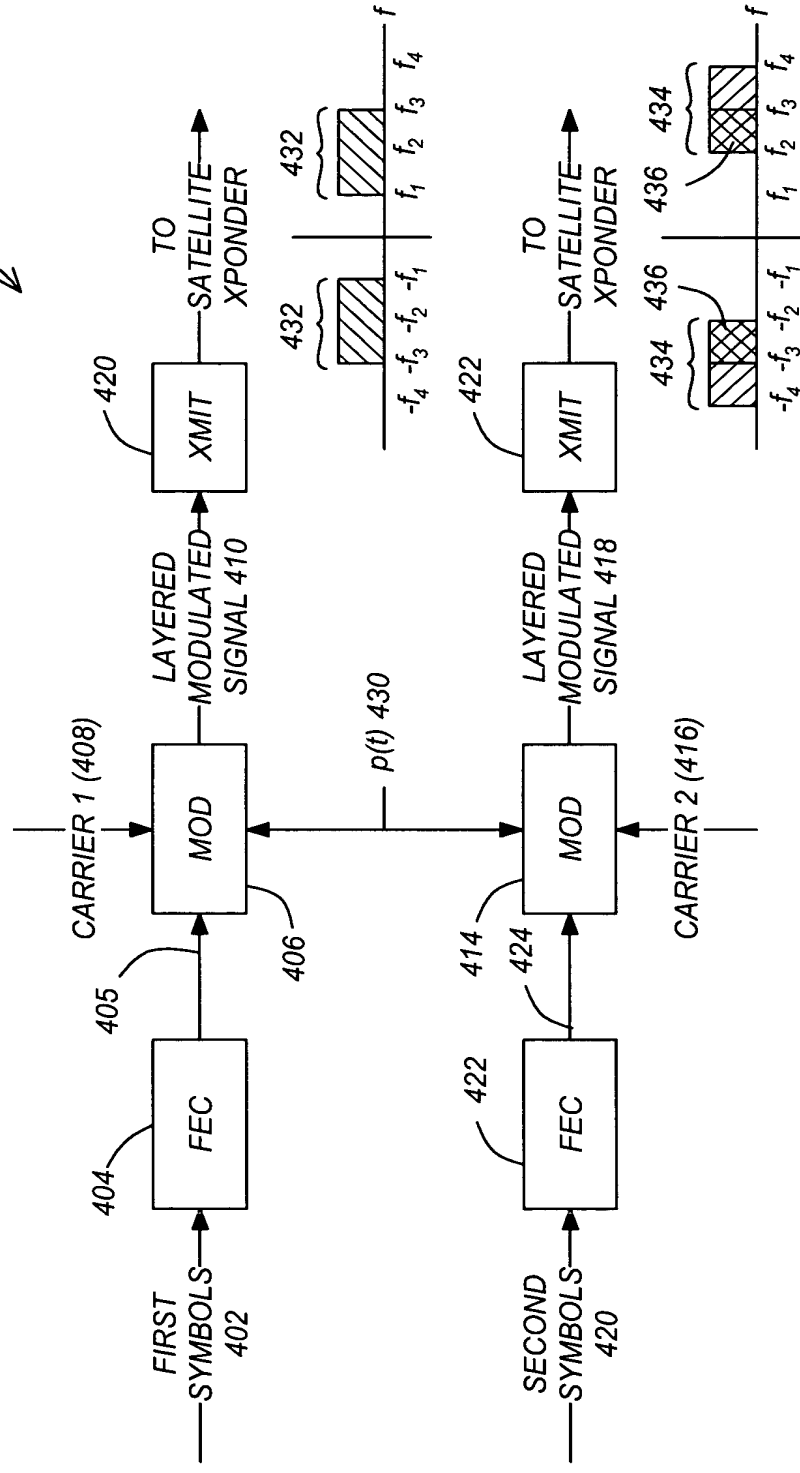


FIG. 4

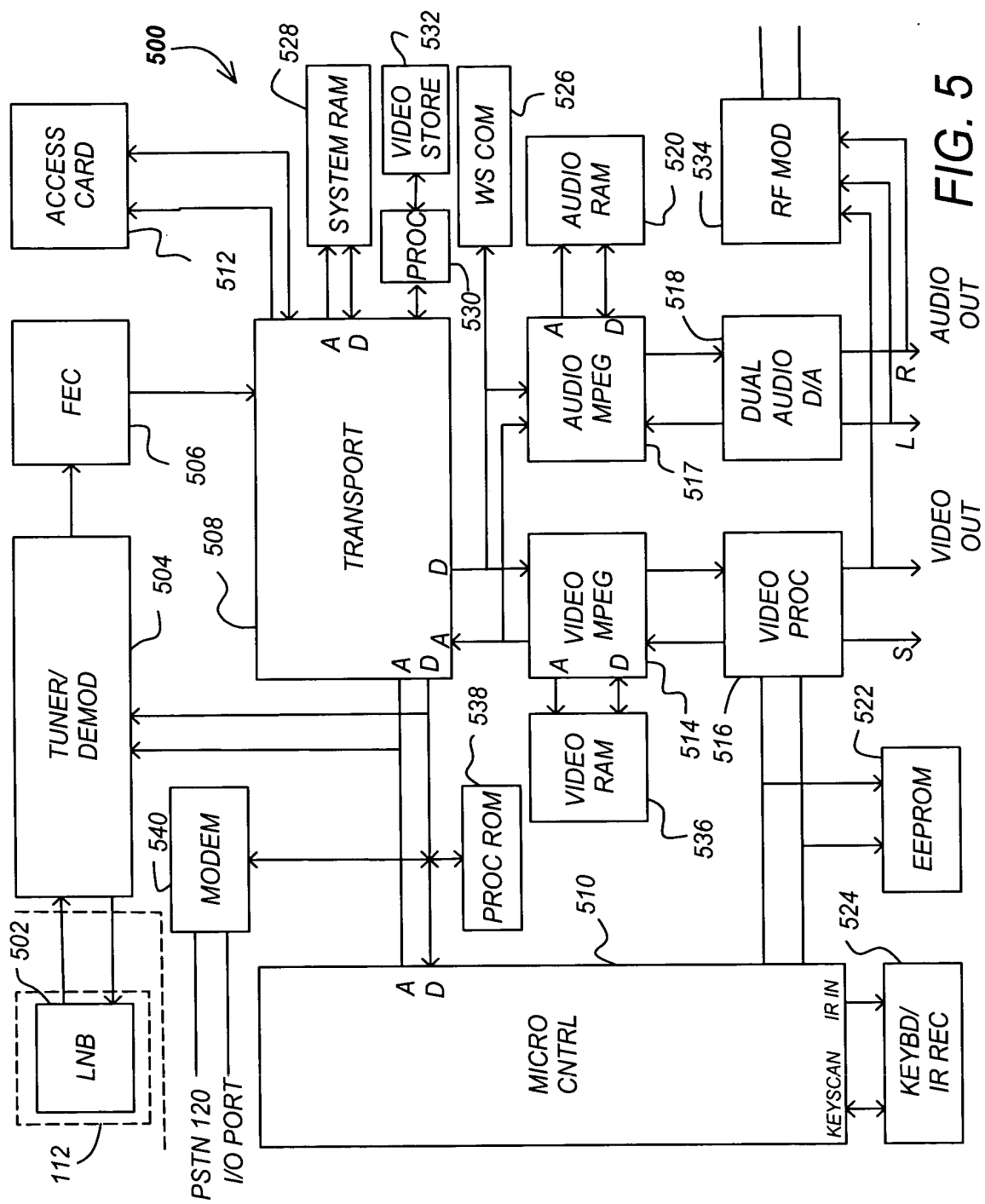


FIG. 5

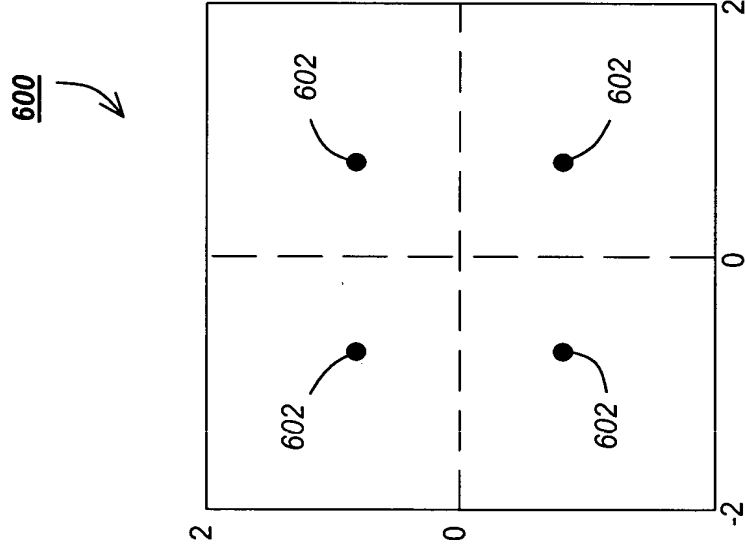


FIG. 6A

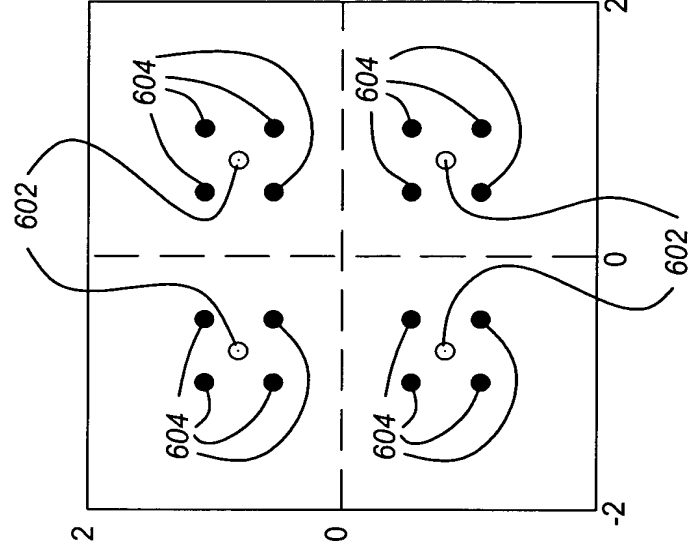


FIG. 6B

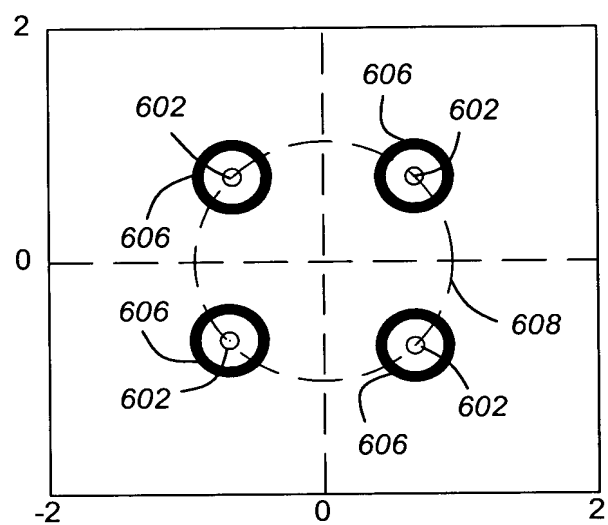


FIG. 6C

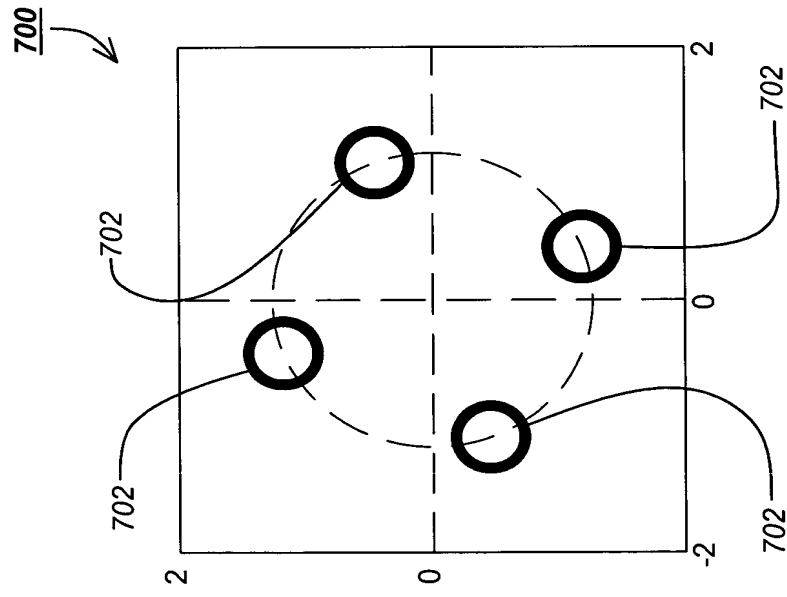


FIG. 7A

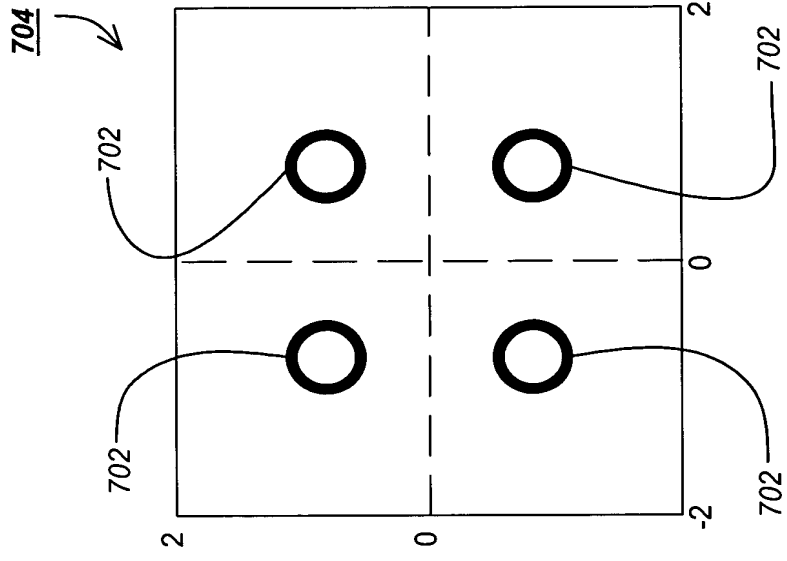


FIG. 7B

Phase Error Distribution

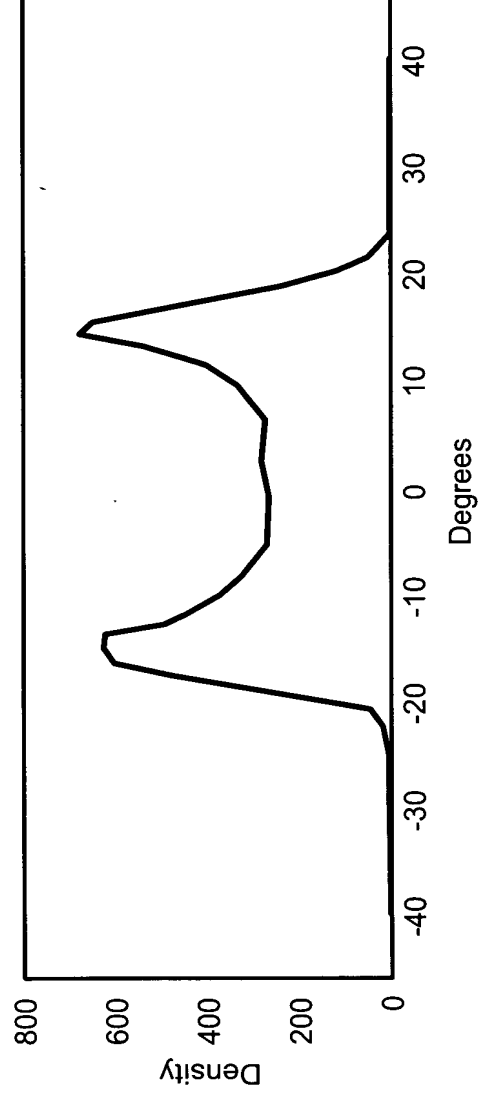
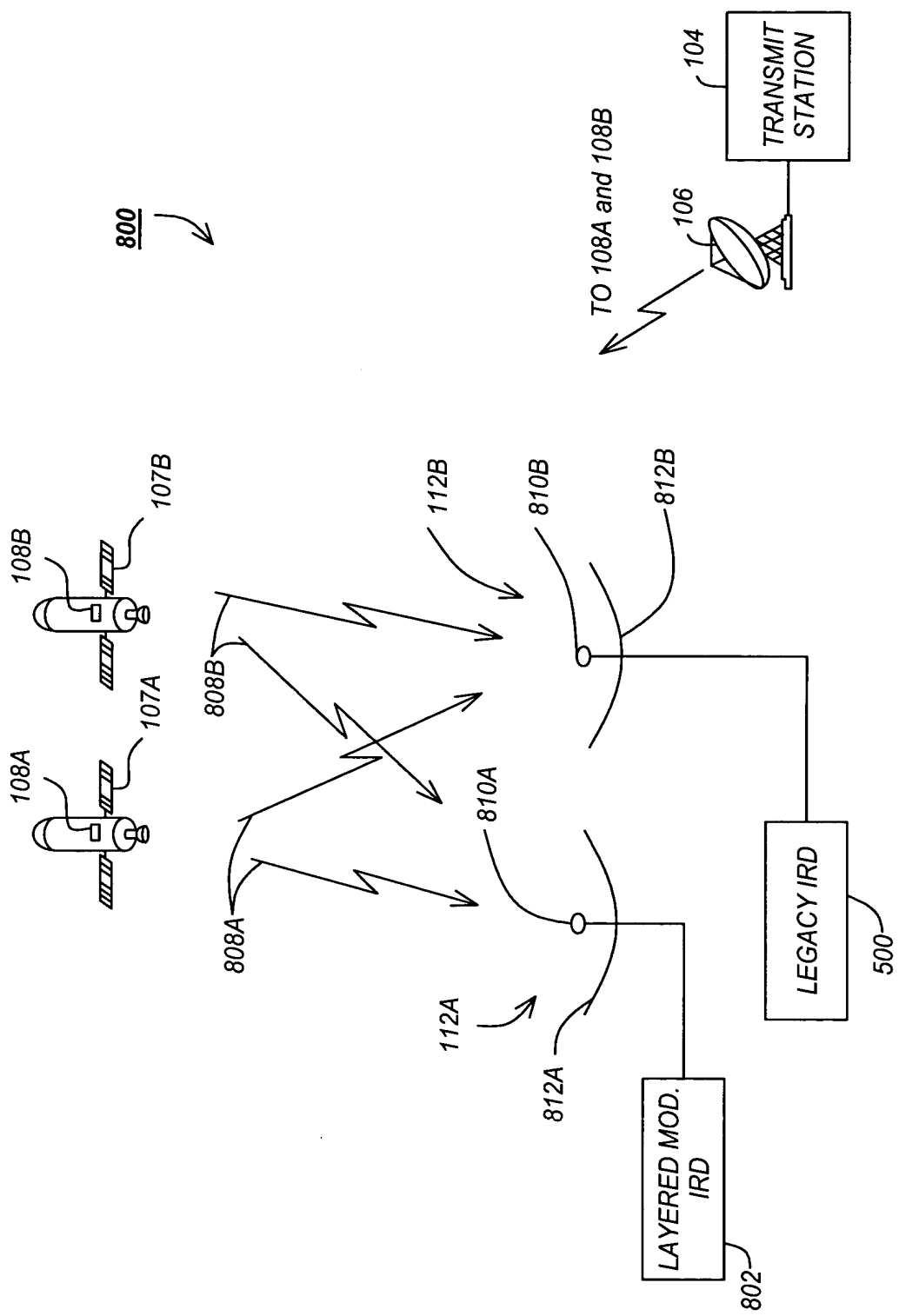


FIG. 7C



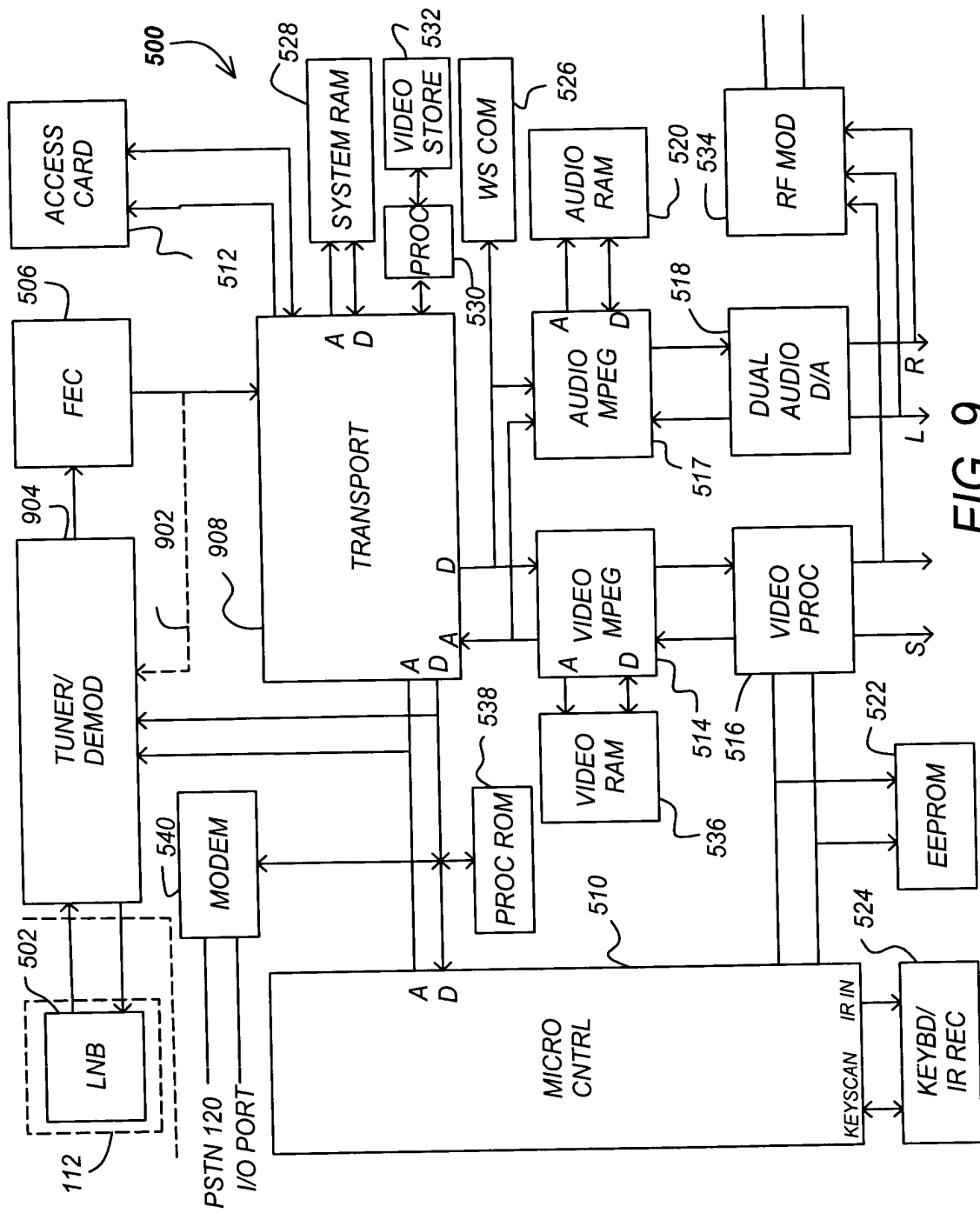


FIG. 9

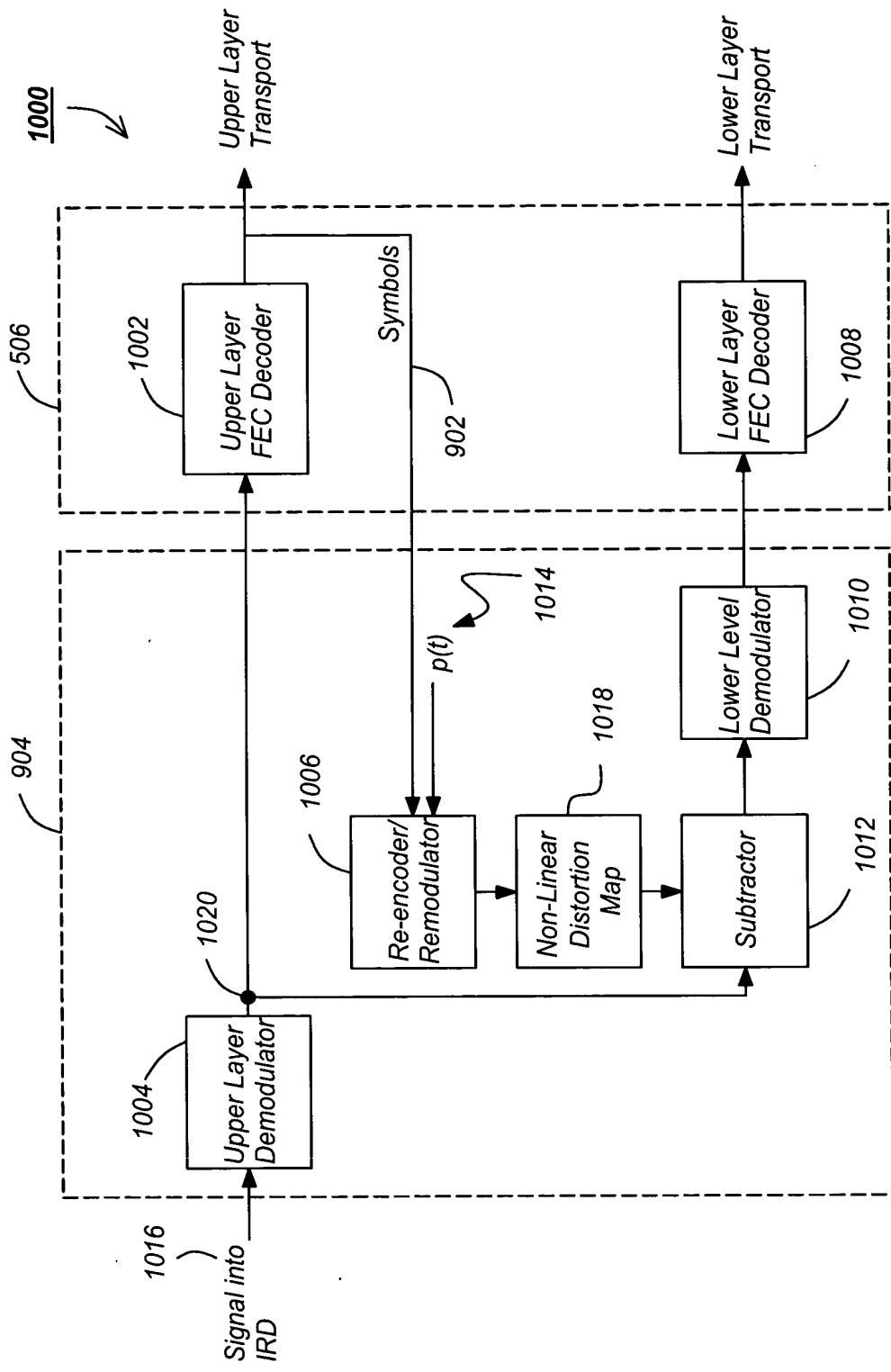


FIG. 10A

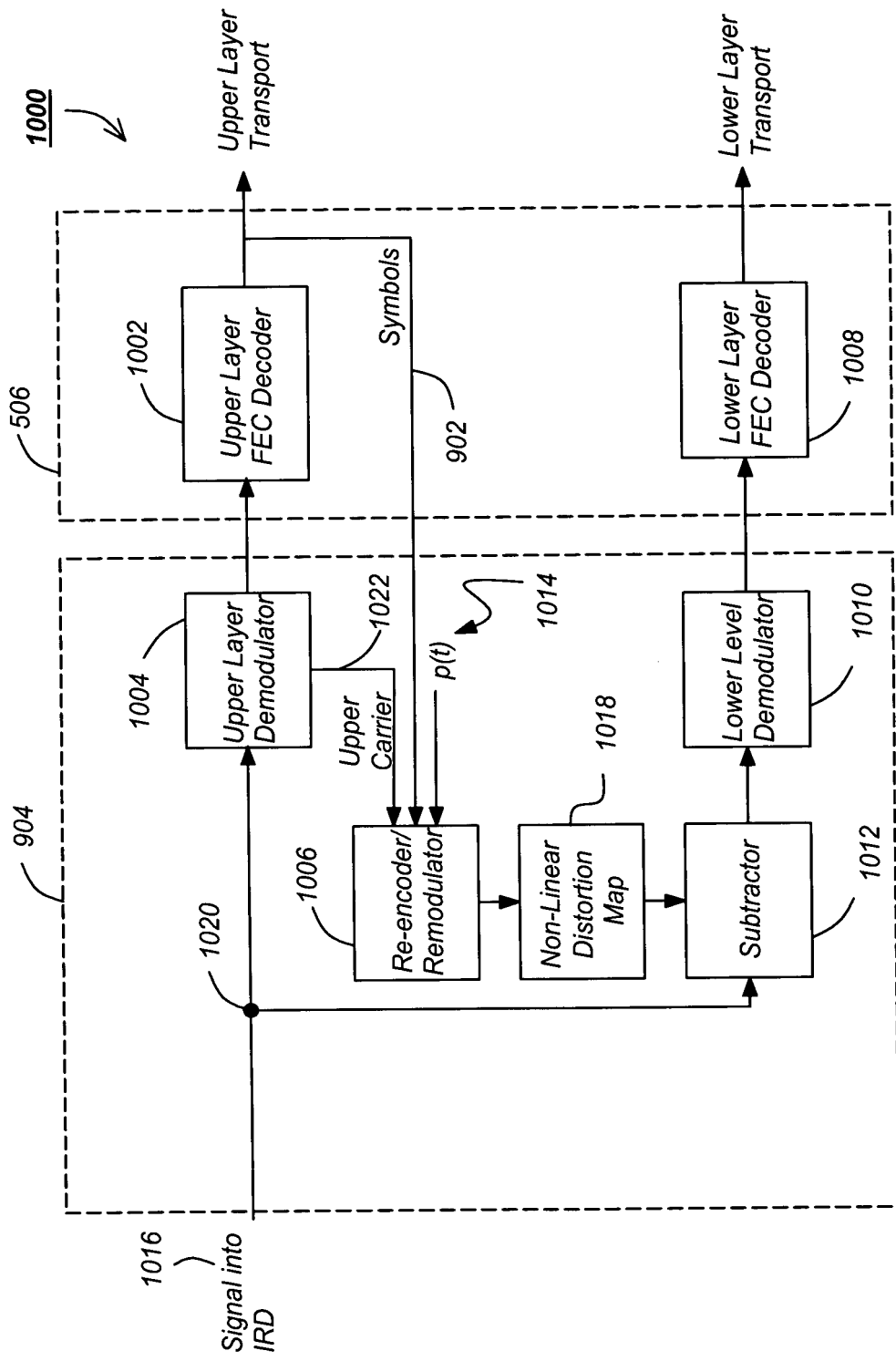


FIG. 10B

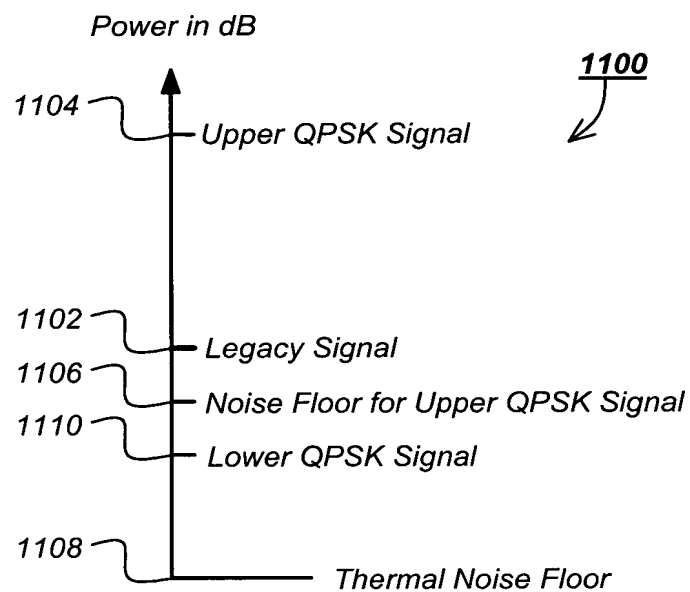


FIG. 11A

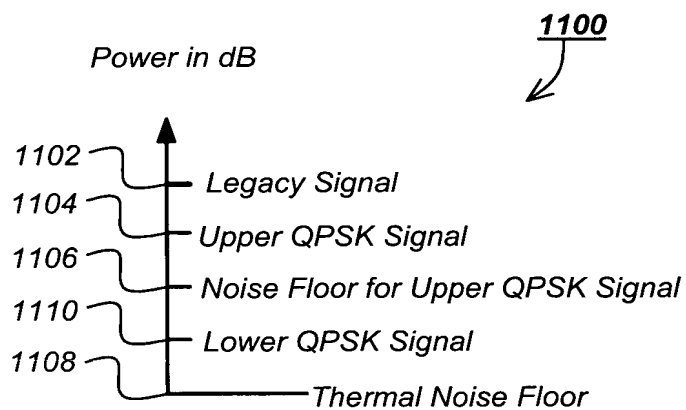


FIG. 11B

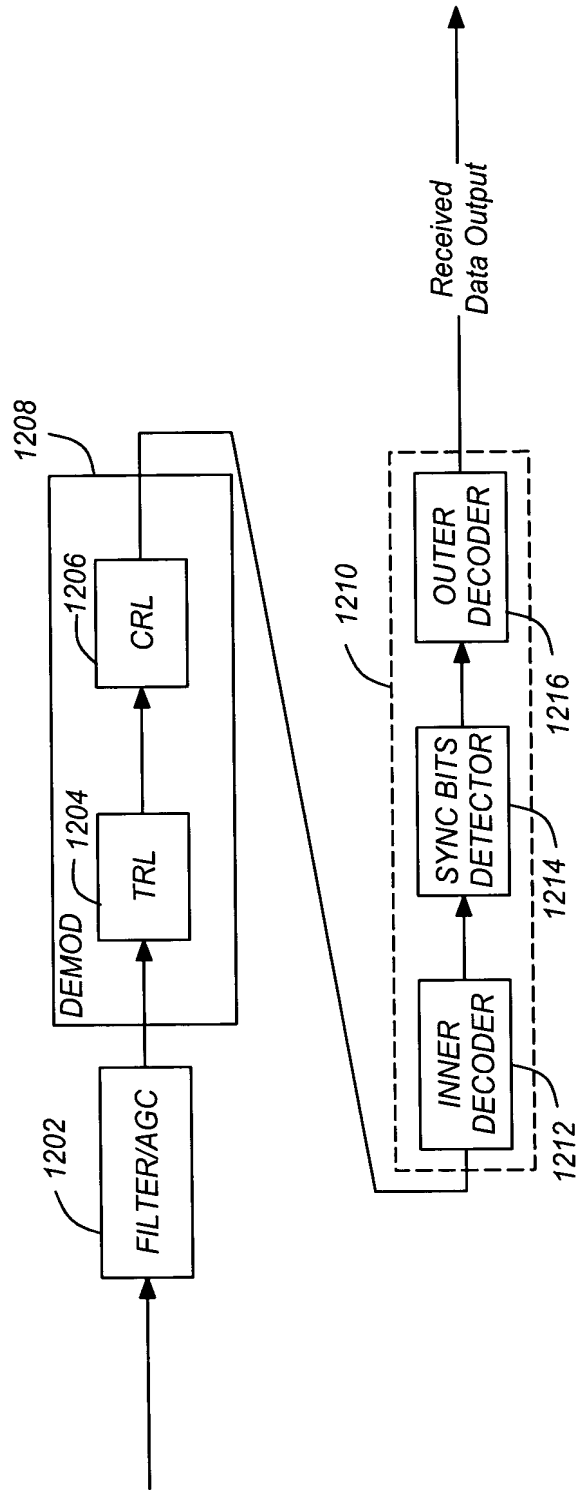


FIG. 12

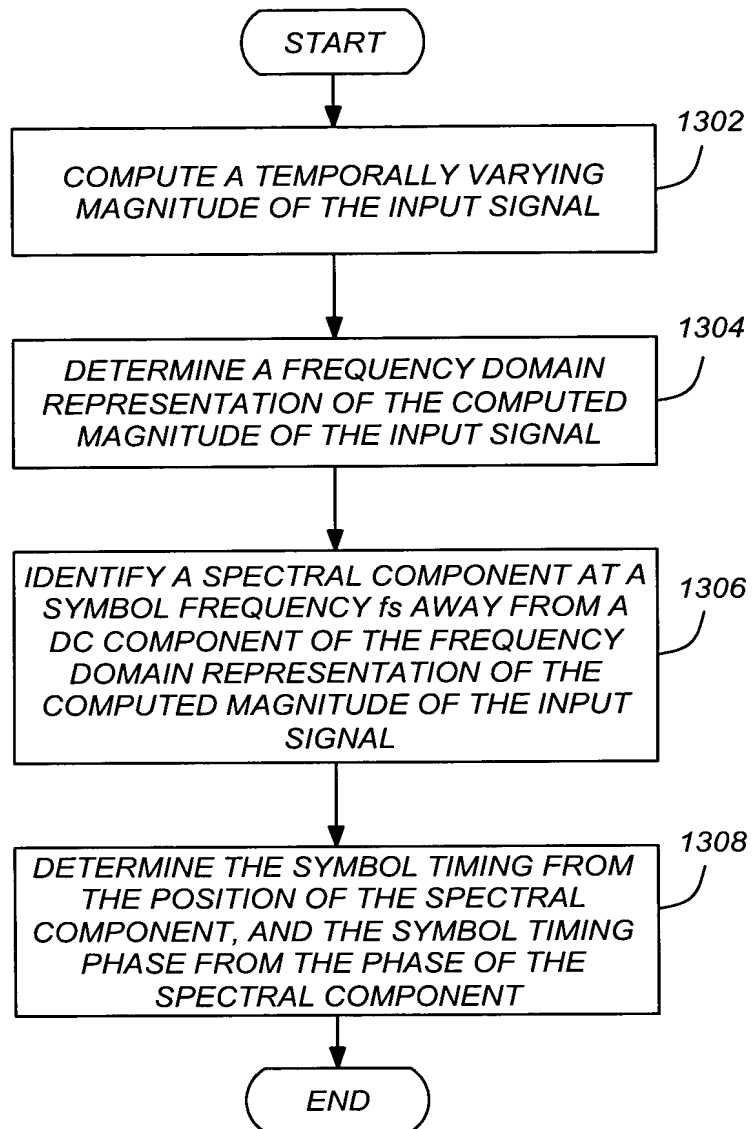


FIG. 13

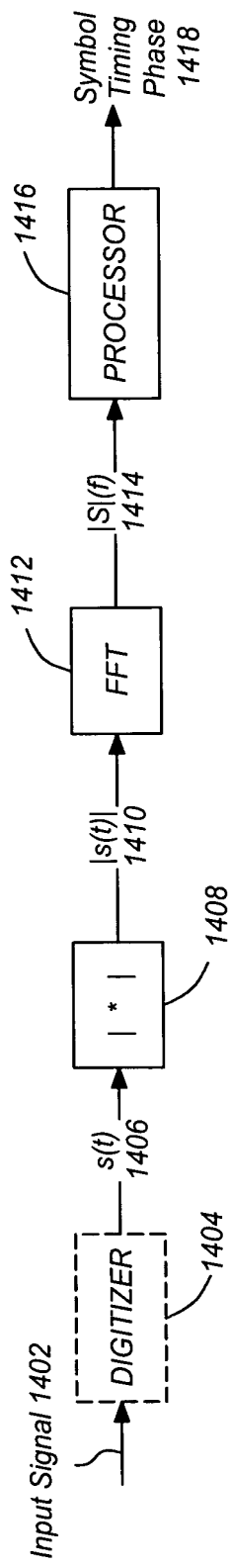


FIG. 14A

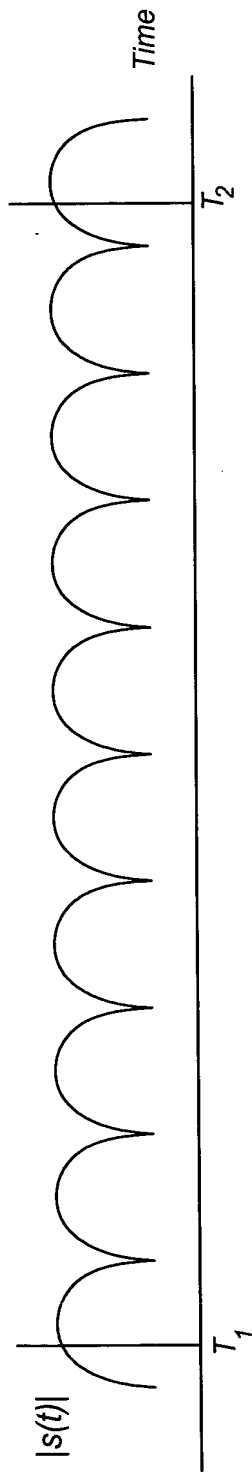


FIG. 14B

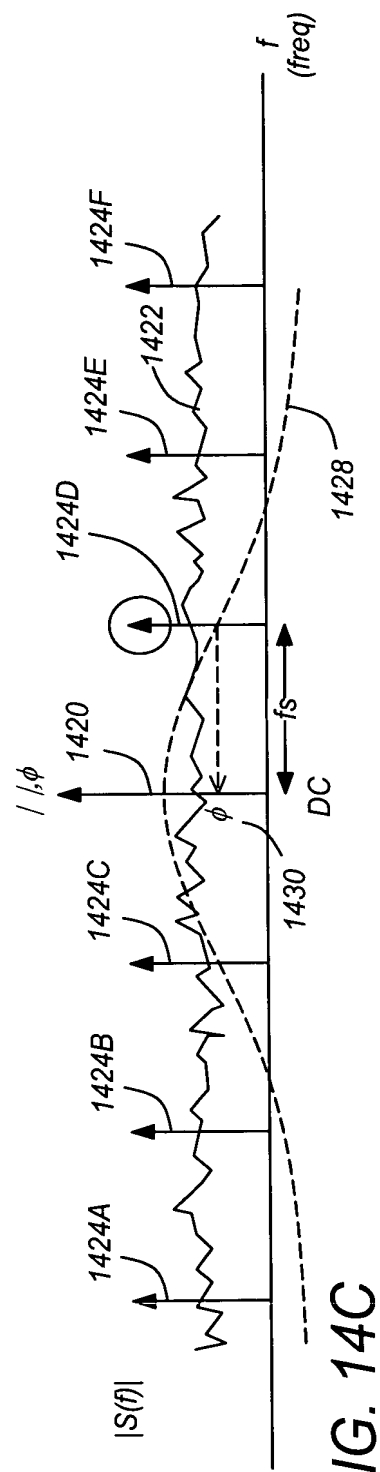
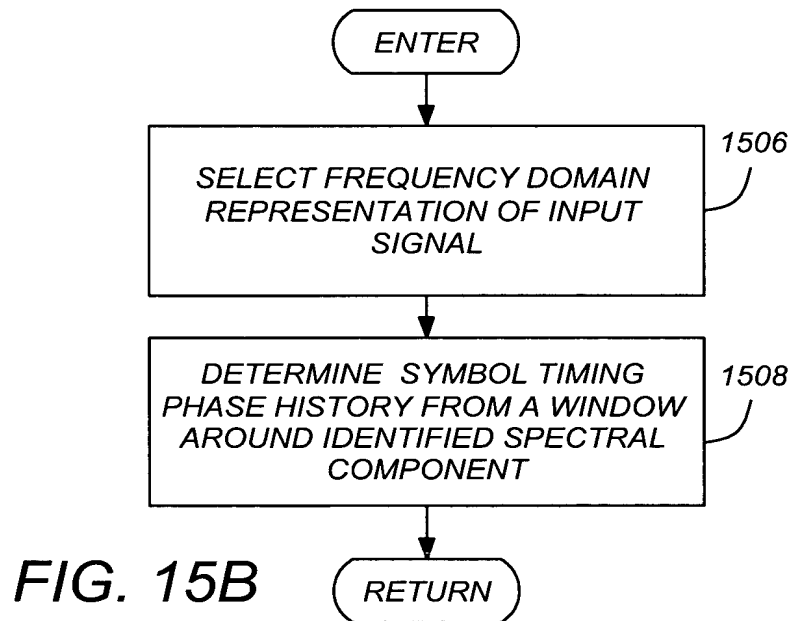
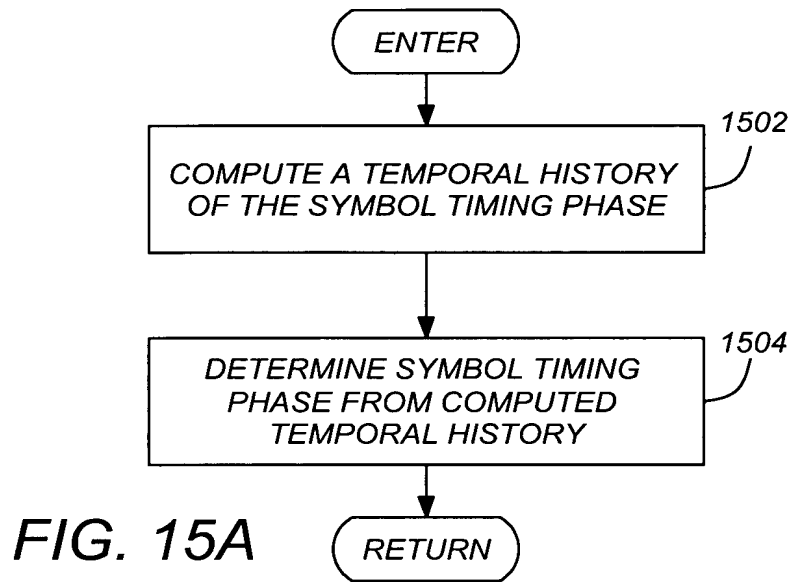


FIG. 14C



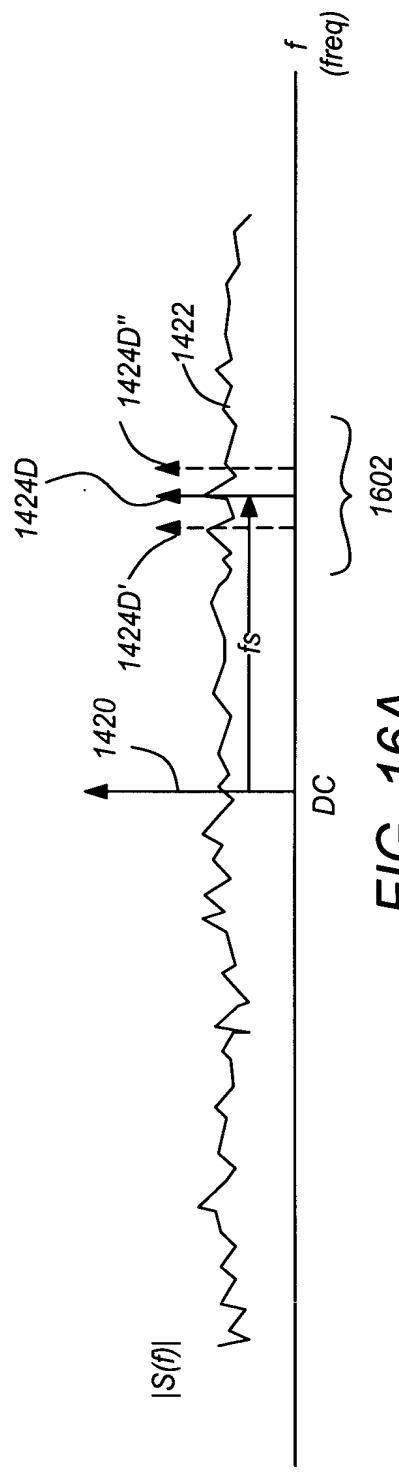


FIG. 16A

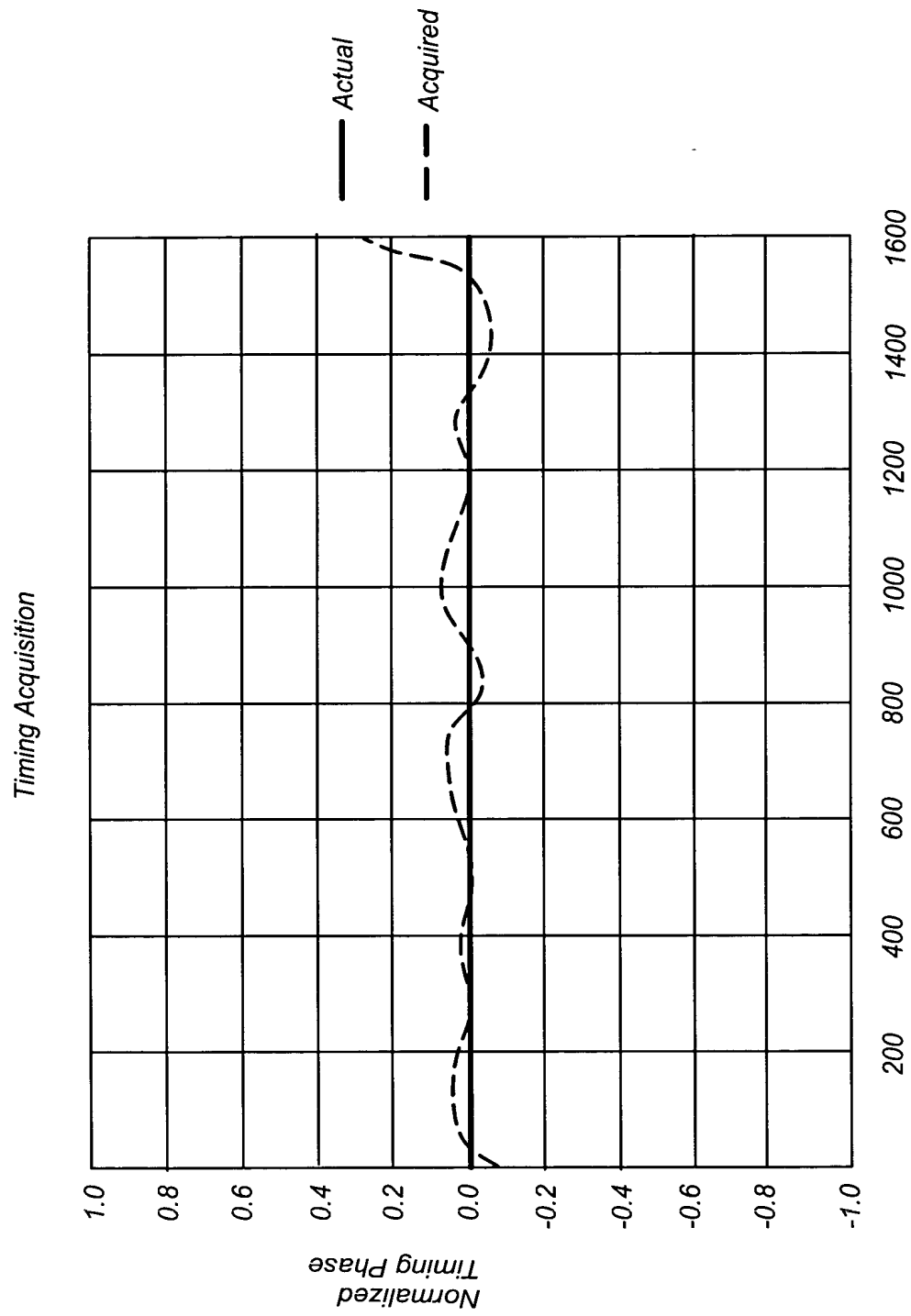
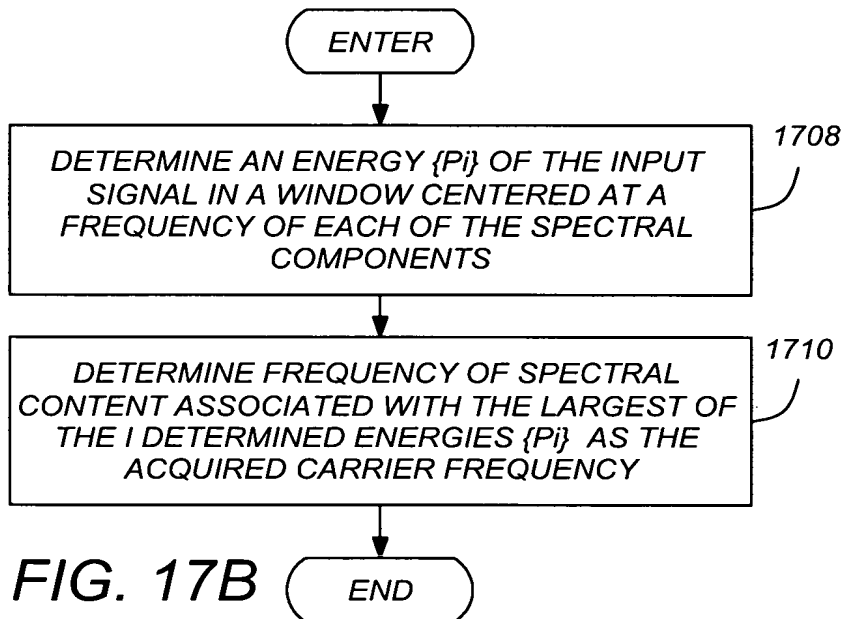
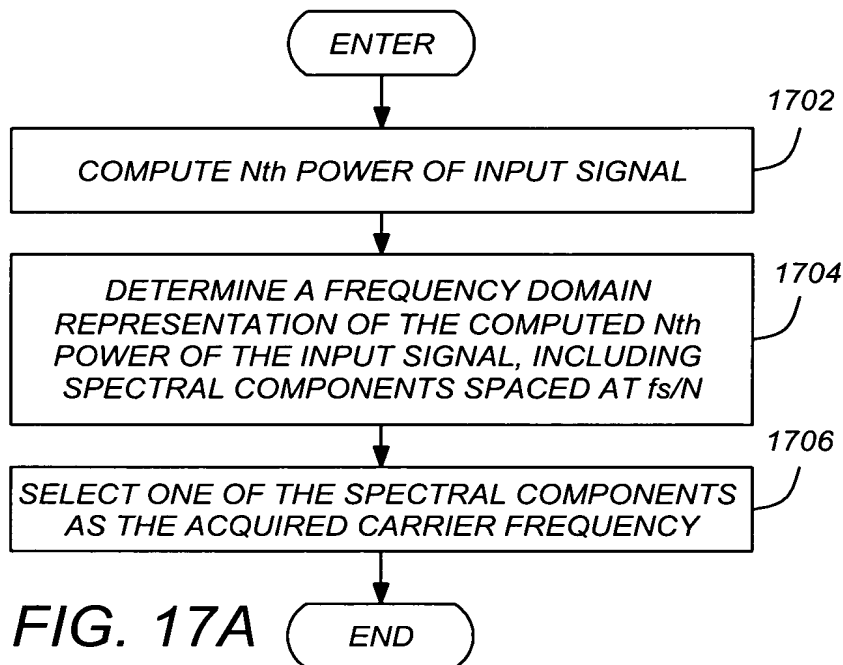


FIG. 16B

Time (Symbol Number)



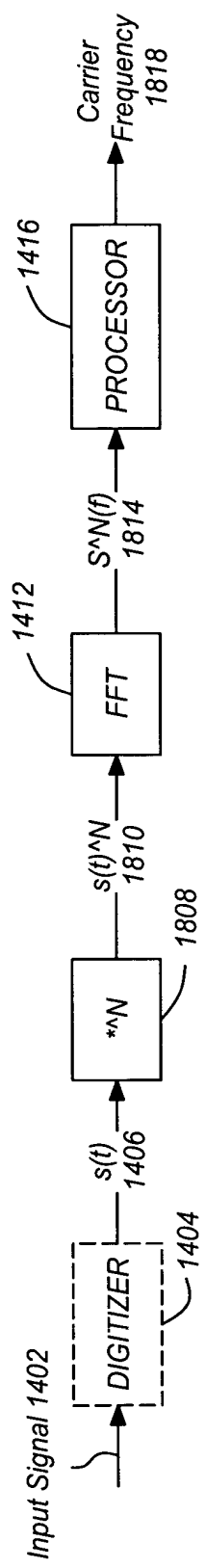


FIG. 18A

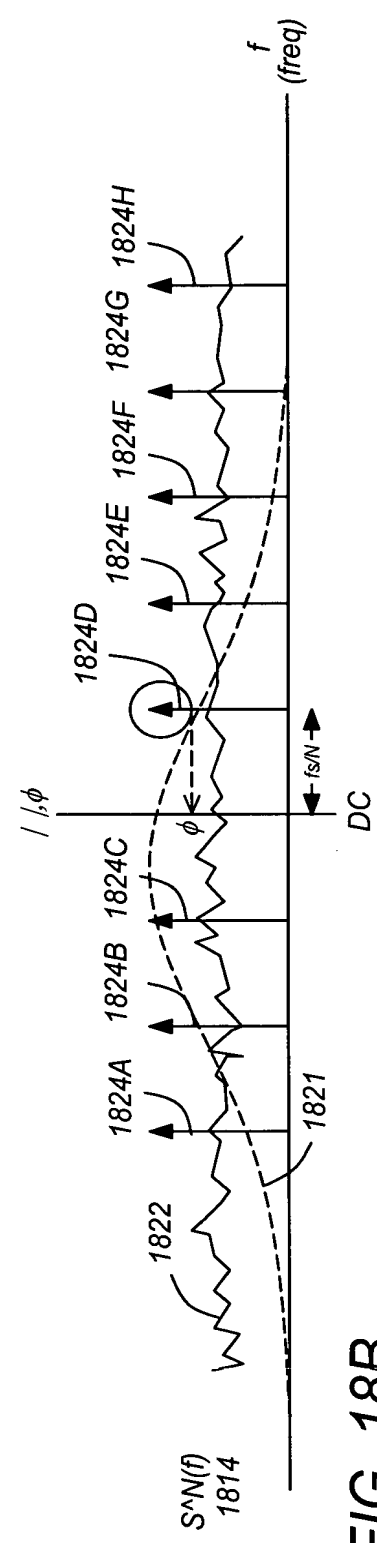


FIG. 18B

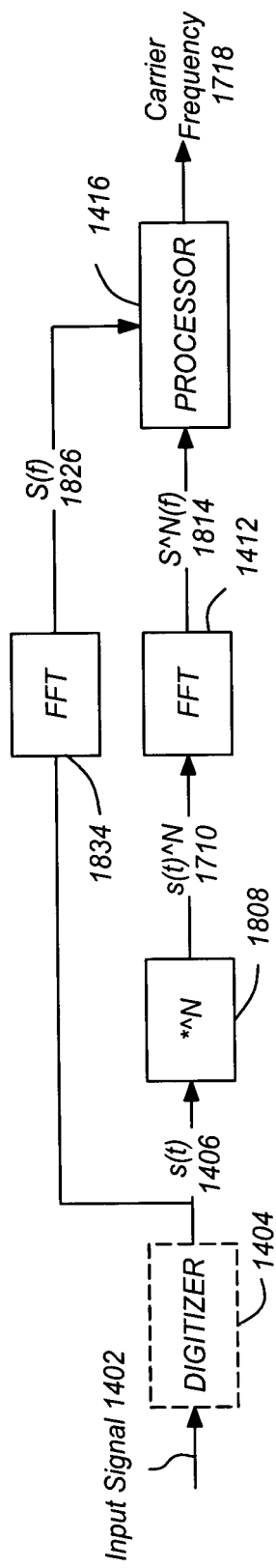


FIG. 18C

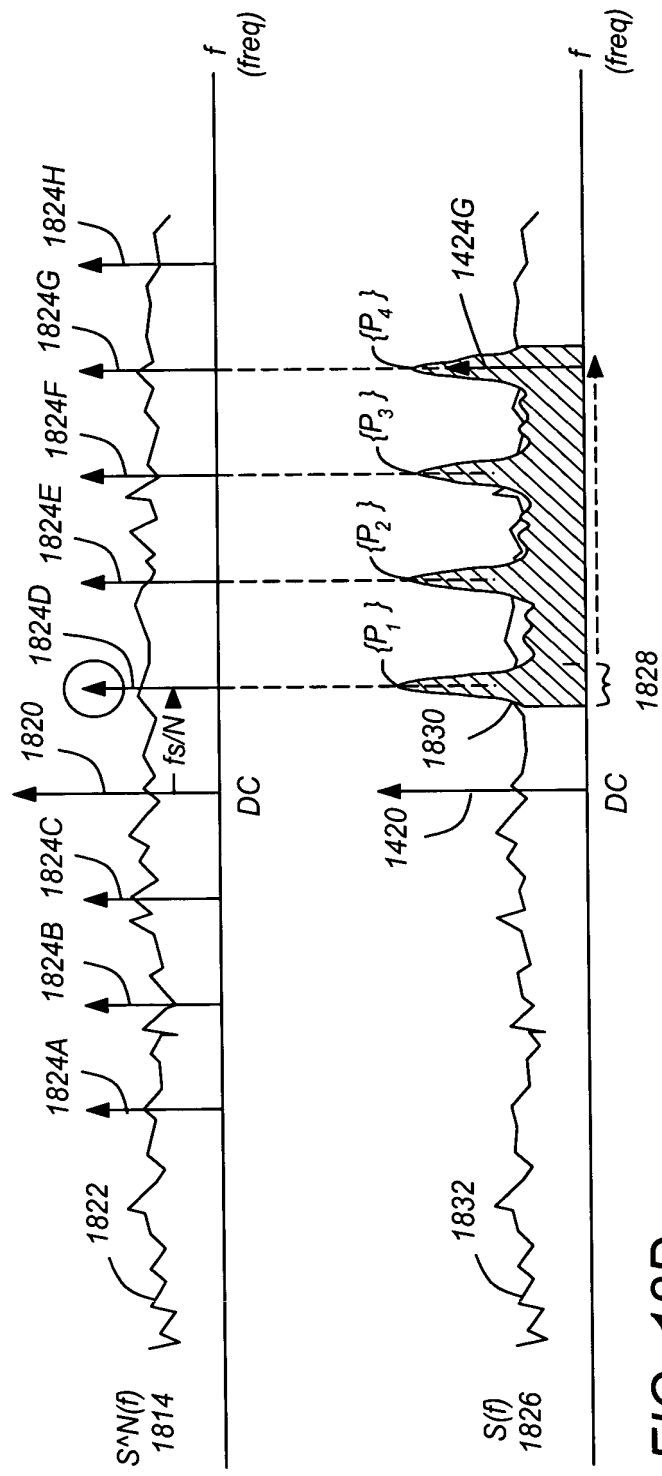


FIG. 18D